

test

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Contents

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[spai@asus-m51ac Downloads]$ unzip -l viva10000-2014.1-rev1-xlnx_lab_files.zip | grep
   0  06-05-14 12:07  training/VDF_PN/labs/advflow_lab/KC705/verilog/
   0  06-05-14 12:07  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.ca
   0  04-15-14 15:41  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.ca
   0  06-05-14 12:07  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.ca
  154 04-15-14 15:42  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.ca
  109 04-15-14 15:41  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.ca
 1417 04-15-14 15:42  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.ca
   0  06-05-14 12:07  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
   0  06-05-14 12:07  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
   0  06-05-14 12:07  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
   0  06-05-14 12:07  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
 2723 04-15-14 15:36  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
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 3781 04-15-14 15:33  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
 1577 04-15-14 15:33  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
 2893 04-15-14 15:33  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
 3526 04-15-14 15:33  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
 2575 04-15-14 15:33  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
 2305 04-15-14 15:33  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
   0  06-05-14 12:07  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
   0  06-05-14 12:07  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
   0  06-05-14 12:07  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
 2439 04-15-14 15:33  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
 1978 04-15-14 15:33  training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
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4450	04-15-14	15:33	training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
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3527	04-15-14	15:33	training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
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272852	04-15-14	15:33	training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.sr
8143	04-15-14	15:41	training/VDF_PN/labs/advflow_lab/KC705/verilog/uart_led.xp